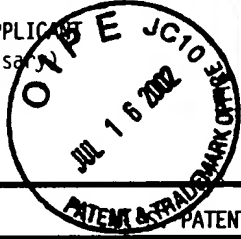| FORM PTO-1449 (REV. 7.80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 00-BN-055	SERIAL NO. 09/751,377
LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT Anthony X. Jarvis	
		FILING DATE December 29, 2000	GROUP 2183

PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
BSO	AA	5,872,986	02/16/1999	Heeb	395	800.01	09/30/1997
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
BSO	AC	EP 0 730 223 A1	09/04/1996	EPO				

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

BSO	AD	TRAINIS, Simon A.: "MODELLING THE HARDWARE COST OF FULL REGISTER BYPASSING IN A MULTIPLE INSTRUCTION ISSUE PROCESSOR": XP 000679755:, Journal of Systems Architecture: 1997 Published by Elsevier Science B.V.; Pgs. 39-46

EXAMINER	DATE CONSIDERED 12/5/03

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.